

MAR 27 1998

U.S. DEPARTMENT OF COMMERCE  
PATENT AND TRADEMARK OFFICE

**REQUEST FOR CORRECTED  
FILING RECEIPT**

Docket Number:  
2885/10

Application Number  
08/947,254

Filing Date  
October 8, 1997

Examiner  
To be assigned

Art Unit  
To be assigned

Invention Title  
**I/O AND MEMORY BUS SYSTEM FOR DFPs AND  
UNITS WITH TWO- OR MULTI-DIMENSIONAL  
PROGRAMMABLE CELL ARCHITECTURES**

Inventor(s)  
Martin VORBACH et al.

Assistant Commissioner  
for Patents  
Washington D.C. 20231

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231 on

Date: 24 March 1998

Reg. No. 36,098

Signature:

  
Michelle M. Carniaux

Applicants respectfully request that the title appearing on the filing receipt be corrected to read as follows:

--I/O AND MEMORY BUS SYSTEM FOR DFPs AND UNITS WITH TWO- OR  
MULTI-DIMENSIONAL PROGRAMMABLE CELL ARCHITECTURES--

A copy of the Filing Receipt with the change shown in red is attached.

Please issue a corrected filing receipt as requested above.

Dated: 24 March 98

By:



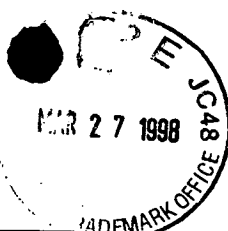
Michelle M. Carniaux (Reg. No. 36,098)

03/31/1998 TWILLIAH 00000010 DAA:110600 08947254  
04 FC:576 25.00 CH

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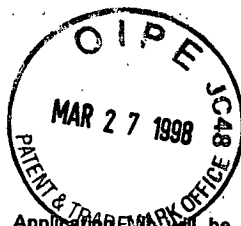
FILING RECEIPT



UNITED STATES DEPARTMENT OF COMMERCE  
Patent and Trademark Office  
ASSISTANT SECRETARY AND COMMISSIONER  
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APPLICATION NUMBER	FILING DATE	GRP ART UNIT	FIL FEE REC'D	ATTORNEY DOCKET NO.	DRWGS	TOT CL	IND CL
08/947,254	10/08/97		\$790.00	2885/10	11	1	1

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ONE BROADWAY  
NEW YORK NY 10004



Receipt is acknowledged of this nonprovisional Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Application Processing Division's Customer Correction Branch within 10 days of receipt. Please provide a copy of the Filing Receipt with the changes noted thereon.

Applicant(s)

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FOREIGN APPLICATIONS-

FED REP GERMANY

196 54 595.1

12/20/96

TITLE

*I/O* ~~IO~~ AND MEMORY BUS SYSTEM FOR DFPS AS WELL AS MODULES HAVING TWO OR  
MULTIDIMENSIONAL PROGRAMMABLE CELL STRUCTURES  
OR MULTI-DIMENSIONAL ARCHITECTURES  
PRELIMINARY CLASS:

RECEIVED  
KENYON & KENYON  
ONE BROADWAY  
NEW YORK, N.Y.  
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